IEEE Asia Pacific Conference on Circuits and Systems (APCCAS2021) &
IEEE Conference on Postgraduate Research in Microelectronics and Electronics (PRIMEASIA2021)

	Malaysia Time		22/11/2021 (Monday): Tutorial & Workshop	
Duration	Start Time	End time	Parallel Session A	Parallel Session B
0:30	8:30	9:00	Registration	
2:00	9:00	11:00	Tutorial: Dye Sensitized Solar Cell by Prof Ir. Dr. Suhaidi Shafie & Dr. Mohd. Amrallah B. Mustafa	Tutorial: Ultra-Low-Power and agile IC design: towards battery-less and low-cost distributed sensing by Dr. Orazio Aiello
0:30	11:00	11:30	Tea Break	
2:00	11:30	13:30	Tutorial: Radio over IP Embedded System by Mr. Wira Firdaus Haji Yaakob	Tutorial: CMOS Device Modeling by Dr. Philip Tan Beow Yew

	Malaysia Time		23/11/2021 (Tuesday)		
Dura-tion	Start Time	End time	Parallel Session A	Parallel Session B	Parallel Session C
0:20	9:30	9:50	Opening Ceremony		
0:45	9:50	10:35	Keynote 1: Akinori Konno, Professor, Graduate School of Science and Technology, Shizuoka University, Hamamatsu, Japan Title: Development of Pre-dyed Dye-sensitized and Perovskite Solar Cells		
0:15	10:35	10:50	Tea Break		
1:40	10:50	12:30	Analog and Mixed Signal Circuits and Systems 1	Digital Circuits and Systems 1	Artificial Intelligent Circuits and Systems
1:30	12:30	14:00	Lunch Break		
0:45	14:00	14:45	Keynote 2: Harikrishnan Ramiah, Associate Professor, University Malaya, Malaysia Title: Radio Frequency Energy Harvesting for Healthcare Wearable		
0:30	14:45	15:15	IEEE CAS Young Membership Elevation Drive by IEEE CAS Malaysia		
2:00	15:15	17:15	PrimeAsia 2021	Imaging System and Technologies & Multimedia Systems and Applications	Neural Networks and Neuromorphic Engineering
0:10	17:15	17:25	Tea Break		
0:20	17:25	17:45	Mentoring Program by IEEE CAS		

Dura-tion	Start Time	End time	Malaysia Time 24/11/2021 (Wednesday)		
			Parallel Session A	Parallel Session B	Parallel Session C
0:45	9:30	10:15	Keynote 3: Shimeng Yu, Associate Professor, School of Electrical and Computer Engineering, Georgia Institute of Technology Title: RRAM for Compute-in-Memory: From Inference to Training		
0:15	10:15	10:30	Tea Break		
0:45	10:30	11:15	Keynote 4: Masami Ikura, Toyota Tsusho Nexty Electronics, Thailand Title: New issues and proposals for new technology development seen from application examples of AI + CONNECTED technology for automotives		
1:40	11:15	12:55	Analog and Mixed Signal Circuits and Systems 2	Digital Circuits and Systems 2	Communication Circuits and Systems & Signal Processing, Control and Communications
1:05	12:55	14:00	Lunch Break		
0:45	14:00	14:45	Keynote 5: Massimo Alioto, Professor, National University of Singapore Title: Intelligent Systems with Ultra-Wide Power-Performance Adaptation - Going Well beyond the Diminishing Returns of Voltage Scaling		
0:45	14:45	15:30	Keynote 6: Dr Bo Chen, Cadence, Singapore Title: The State-of-the-Art and Next Generation Simulation Technologies for Analog, Mixed-Signal and Memory Design		
0:10	15:30	15:40	Tea Break		
2:00	15:40	17:40	RF Integrated Circuit Design and Energy Harvesting	Digital Circuits and Systems & Digital Signal Processing	Nano electronics, Devices and System Integration & Sensors and Interfaces & Automotive Circuits and Systems
0:20	17:40	18:00	Mentoring Program by CAS		

Dura-tion	Start Time	End time	Parallel Session A	Parallel Session B	Parallel Session C
	Malaysia Time		25/11/2021 (Thursday)		
0:45	9:30	10:15	Keynote 7: Pui-In (Elvis) Mak, Professor, University of Macau, China Title: Towards Energy-Autonomous Bluetooth-Low-Energy Radios for IoT Applications		
0:15	10:15	10:30	Tea Break		
0:45	10:30	11:15	Keynote 8: Chip Hong Chang, Associate Professor, Nanyang Technology University (NTU) of Singapore Title: Security of Edge AI – A new challenge to deep learning accelerators		
1:20	11:15	12:35	Analog and Mixed Signal Circuits and Systems 3	Biomedical and Healthcare Circuits and Systems	Power/Energy Devices, Circuits and Systems & Integrated Power Management Unit (PMU)
1:25	12:35	14:00	Lunch Break		
0:45	14:00	14:45	Keynote 9: Mr Suresh, VP of Design Center, Intel Title: Technology as an Enabler For Sustainable Growth – The Opportunity Ahead		
1:40	14:45	16:25	GeronCAS Workshop	Analog and Mixed Signal Circuits and Systems 4	Electronic Design Automation & High Speed and Optical Wire lined Circuits and Systems
0:15	16:25	16:40	Tea Break		
0:20	16:40	17:00	Closing Ceremony		