

6th Version of DevIC Series of IEEE Int. Conf. on "2025 Devices for Integrated Circuit" DevIC 2025 (IEEE Record No. 63749)

Organized by

IEEE KGEC SB Chapter, IEEE EDS Kolkata Chapter and Dept. of ECE, KGEC
in Technical Partnership with IETE Kolkata Chapter
Date: 5th –6th April, 2025

Venue: Kalyani Govt. Engg. College, Kalyani, West Bengal, India

Conference url: <https://attend.ieee.org/devic-2025/>

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Accepted and presented papers will be submitted for inclusion into IEEE Xplore subject to meeting IEEE Xplore's scope and quality requirements

Few Selected Extended version of the presented papers will be sent to SCI/ Scopus Journals and book Chapters.

Topics of Interests but not limited to

Track 1: CMOS Processes, Devices and Integration

Track-2: VLSI Technology and Circuits

Track 3: Innovative Systems

Track-4: Emerging Non-CMOS Devices & Technologies

Track 5: Device Modelling & Simulation

Track 6: Device Characterization, Reliability & Yield

Track 7: Devices with New material systems

Track 8: Devices for Low power applications

Track 9: Low dimensional devices

Track 10: Design and Simulation of Circuits with nanoscale devices

Track 11: MEMS, Sensors & Display Technologies

Track 12: Advanced & Emerging Memories

Track 13: High frequency wireless communication

Link for paper submission:

<https://cmt3.research.microsoft.com/DevIC2025>

Important Deadline

Last date for paper submission: **31st January 2025**

Last Date for acceptance: **1st March 2025**

Camera-ready paper submission: **15th March 2025**

Registration Fees

Category	From India (in Rs)		From Abroad (in \$)	
	IEEE	NON-IEEE	IEEE	NON-IEEE
Authors				
Student	5000	5500	150	200
Academ. /Indust.	6000	6500	250	300

