

Estimado Participante:

Te felicitamos por tu participación en el EDS Summer School 2024. Para incrementar tu conocimiento a través del curso se recomienda enfáticamente que puedas ver los siguientes videos antes de iniciar el curso.

URL de Grupo de Whatsapp:

<https://chat.whatsapp.com/CxTlypa1aQtllm9toA31I7>

Introduction to semiconductor fabrication:

Favor ver con atención los siguientes videos:

[📺 Como se fabrica un Circuito Integrado - Nace un Chip](#)
[La Manufactura de un Semiconductor - Texas Instruments](#)

Introduction to Digital design and verification:

Este video generado por e-Fabless realiza una introducción al diseño digital de circuitos integrados haciendo uso de herramientas open source. Además, muestra cómo enviar tu diseño para fabricación por un costo de \$150 USD.

[Webinar - Build Your First Chip with Tiny Tapeout](#)

Tiny Tapeout:

Videos generados por Matt Venn donde muestra algunos ejemplos de diseños de circuitos integrados. Durante el curso del Summer School se mostrarán herramientas más avanzadas por personal de Synopsys.

[TinyTapeout 3.5 multiplexer and projects demo!](#)



Day One May 4th 2024				
Start	Finish	Duration	Description	Room
7:30	8:30	1:00	Registration	401 T1
8:30	9:00	0:30	Opening remarks by Summer School Chair Oscar Rodas - Universidad Galileo	401 T1
9:00	10:00	1:00	Introduction to the Design of digital integrated circuits and RTL Writing Victor Grimblatt - Synopsys	401 T1
10:00	10:20	0:20	Coffee Break AM	401 T1
10:20	13:00	2:40	Integrated circuit design using Verilog Victor Grimblatt - Synopsys	401 T1
13:00	14:00	1:00	Lunch	Pérgola
14:00	15:30	1:30	Verification, Synthesis and optimization of Digital Integrated Circuits - Part 1 Victor Grimblatt & Esteban Viveros - Synopsys	401 T1
15:30	16:00	0:30	Coffee Break PM	401 T1
16:00	18:00	2:00	Verification, Synthesis and optimization of Digital Integrated Circuits	401 T1





			Victor Grimblatt & Esteban Viveros - Synopsys	
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Day Two May 5th 2024				
Start	Finish	Duration	Description	Room
8:30	10:15	1:45	Low Power Design Techniques and Circuit Testability Victor Grimblatt & Esteban Viveros - Synopsys	401 T1
10:15	10:30	0:15	Coffee Break	401 T1
10:30	12:30	2:00	Place & Route, Signoff (Part 1) Esteban Viveros - Synopsys	401 T1
12:30	13:30	1:00	Lunch	Pérgola
13:30	14:15	0:45	Place & Route, Signoff (Part 2) Esteban Viveros - Synopsys	401 T1
14:15	15:30	1:15	Open Source Fabrication and LatinPractice Camilo Vélez - Latin Practice	401 T1
15:30	15:45	0:15	Coffee Break	401 T1
15:45	16:45	1:00	The Semiconductor Industry and Opportunities for Latin America Fernando Guarín - IEEE EDS	401 T1



16:45	17:30	0:45	IC Reliability Fernando Guarín - IEEE EDS	401 T1
17:30	18:15	0:45	Control systems for IC Manufacturing and fabrication challenges Wilfrido Moreno - University of South Florida	401 T1

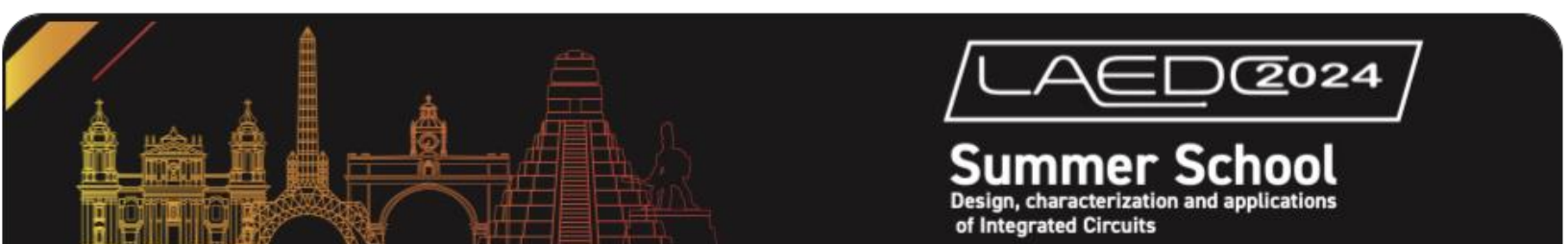
Day Three May 6th 2024				
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Start	Finish	Duration	Group 1	Group 2	Room	
8:30	10:30	2:00	Designing a Professional PCB for STEM Education (Part 1): Altium Designer Cristian Aguilar - Universidad Galileo	Designing a Professional PCB for Monitoring Air Quality (Part 1): Kicad Ángel Isidro - Universidad Galileo	Group 1 - Lab 202 T1 Group 2 - Lab 215 T1	
10:30	11:00	0:30	Coffee Break			Nivel 2
11:00	13:00	2:00	Designing a Professional PCB for STEM Education (Part 2): Altium Designer Cristian Aguilar - Universidad Galileo	Designing a Professional PCB for Monitoring Air Quality (Part 2): Kicad Ángel Isidro - Universidad Galileo	Group 1 - Lab 202 T1 Group 2 - Lab 215 T1	
13:00	14:00	1:00	Lunch			Pérgola
14:00	16:00	2:00	Guided Tour: Robotics Labs (2), Electronics Labs with Virtual Instrumentation & PCBA Demo	Guided Tour: Robotics Labs (2), Electronics Labs with Virtual Instrumentation & PCBA Demo	Laboratories	
16:00	16:30	0:30	Solving problems for industry - PipEnspector Julio Fajardo - Universidad Galileo			401 T1
16:30	17:00	0:30	Coffee Break			401 T1





17:00	17:30	0:30	Building and IoT Low Cost Sensor Air Quality Monitoring Network - MONAIR <i>Oscar Rodas - Universidad Galileo</i>	401 T1
17:30	18:00	0:30	Workshop Closing and Feedback Session	401 T1



LAEDC 2024

Summer School
Design, characterization and applications
of Integrated Circuits