

**CALL for PAPERS**



**ADDITIONAL EXTENSION**

**Abstract & 3-page Paper Due: June 7, 2024**

Submit your materials here:

<https://epapers2.org/naecon2024>

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**Welcome to the 76<sup>th</sup> year of the IEEE National Aerospace and  
Electronics Conference (NAECON)**

**July 15 – 18, 2024**

**In-Person at the Holiday Inn Conference Center in Fairborn, OH**

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This year's theme is:

***Machine Learning/Artificial Intelligence for DoD System/Sensor Collaboration***

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**A DOD breakout session will be held Friday July 19 at Wright Patterson AFB, OH.**

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**Sponsored by:**

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IEEE Circuits & Systems Society (CAS)  
IEEE Aerospace & Electronics Systems Society (AESS)  
IEEE Microwave Theory & Technology Society (MTT-S)



## TECHNICAL SESSIONS & CHAIRS

<b>AES Focused Sessions:</b>		
AES1	<b>Aerospace Power Systems and Power Electronics</b>	Bang Hung-Tsao, UDayton Roshan Kini, PNNL
AES2	<b>Autonomous Systems</b>	Vijayan Asari, UDayton Tim Machin, AFIT
AES3	<b>Integrated Photonics</b>	Andrew Sarangan, UDayton Jarrett Vella, AFRL/RVD
AES4	<b>Radar, Tomography and RF Sensing</b>	John Malas, UDayton Brian Rigling, UDayton
AES5	<b>Machine Learning, Guidance and Control</b>	Trevor Bihl, AFRL/RV Anca Ralescu, UCincinnati Jay Wilhelm, OhioU
AES6	<b>Millimeter-Wave and Terahertz Technology</b>	Elliott Brown, WSU Mike Saville, AFIT
AES7	<b>Cyber Systems and Security Chair</b>	Mark Reith, AFIT Frank Wolff, KBR
<b>CAS Focused Sessions:</b>		
CAS1	<b>Low SWaP/Form Factor, Distributed, and Agile Sensors</b>	Marc Hoffman, AFRL/RVD Stefan Westberg, AFRL/RVD
CAS2	<b>Manufacturing, Packaging, and Integration of Emerging Microelectronics</b>	Vamsy Chodavarapu, UDayton Amy Doll, UDayton Robert Ewing, AFRL
CAS3	<b>Signal/Image Processing and Sensor Data Fusion</b>	Ken Hopkinson, AFIT Barath Narayanan, UDayton Bradley Ratliff, UDayton
CAS4	<b>Deep Learning and Artificial Intelligence</b>	David Kapp, AFRL Tem Kebede, AFRL
CAS5	<b>Quantum Computing, Communication, and Sensing</b>	Charles Cerny, AFRL Leleia Hsia, AFIT
CAS6	<b>Electronics in Extreme Environments/Applications and WBG Semiconductors</b>	Ahmed Islam AFRL/RV William Kennedy, AFRL/RX
CAS7	<b>Enabling Hardware for Artificial Intelligence</b>	Ken Hopkinson, AFIT William Kennedy, AFRL/RX
<b>Special Sessions:</b>		
SS1	<b>Trusted Microelectronic Systems</b>	Nicholas Kovach, AFRL/RV Frank Wolff, KBR
SS2	<b>Systems, Human and Cybernetics</b>	Amy Doll, UDayton Robert Ewing, AFRL/RV

# CALL FOR PAPERS

- Prospective authors are invited to submit a **50-word Abstract** and a minimum of a **3-page Paper** (6-page maximum) for consideration by **June 7, 2024**.
- Submissions will be made electronically in PDF format (**IEEE Xplore compatible PDF**) to the following website: <https://epapers2.org/naecon2024>

- **GOVERNMENT AUTHORS ONLY:** If Government Public Release Approval will delay your submittal of the proposed 3-page paper, [please submit now](#) at least a **one-page abstract with title and authors as a placeholder**, approval subject to full paper final acceptance. Submit to Epapers using **Track #4: DOD Submissions**. Abstracts will be reviewed by government-only reviewers.

- All submissions will be considered by the Technical Program Committee. All papers will be judged with respect to their quality, originality, and relevance to NAECON 2024.
- Each submitted paper should identify the “**Subject Area**” and include information about the **contact author** (name, mailing address, phone, and email).
- Authors should obtain company and government clearances **prior to** submission of abstracts and full papers.
- **Each accepted paper requires a full author registration.** An author will be allowed to register for one paper only with his/her main registration. Authors need to have full registration to attend and present a paper, even if another coauthor has registered a shared paper as an additional submission.
- Accepted papers will be published in the conference proceedings and accessible through IEEE Xplore, subject to advance registration of at least one of the listed authors, and presentation of the papers.
- **Revised final papers are due by July 5, 2024.**
- IEEE copyright release forms are required with the final camera-ready papers.
- Nominations will be solicited for the D.W. Repperger Best Paper Award (POC: Dr. Robert Ewing). The best student paper will be selected, and a Best Student Paper Award conferred.

## REGISTRATION OPENS SOON!

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- **Registration opens June 3, 2024** (details will be posted soon!)
- **NAECON 2024 Early Registration through July 5, 2024**
  - IEEE Member (\$550)
  - Non-IEEE Member (\$650)
  - IEEE Student Member (\$300)
  - Non-IEEE Student Member (\$350)
- Cost includes Tutorials, Lunches, Banquet Dinner, and access to the Conference Proceedings.
- Price increases by \$75 after the early registration deadline.